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(Not for submission under 37 CFR 1.99)

10/06/06

Application Number		10757197
Filing Date		2004-01-14
First Named Inventor	DeWitt, Jr. et al.	
Art Unit	2192	
Examiner Name	/Thuy Dao/	
Attorney Docket Number	AUS920030556US1	

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TD	1	6026235		2002-02-15	Shaughnessy	
↓	2	6594820	B1	2003-07-15	Ungar	
↓	3	5950003		1999-09-07	Kaneshiro et al.	

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↓	1	20030066055	A1	2003-04-03	Spivey	
↓	2	20030131343	A1	2002-07-10	French et al.	
↓	3	20040268316	A1	2004-12-30	Fisher et al.	

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TD <div></div>	1	5761103		1998-06-02	Oakland et al.	
	2	6549959	B1	2003-04-13	Yates et al.	
	3	6772322	B1	2004-08-03	Merchant et al.	
	4	5937437		1999-08-10	Roth et al.	
	5	6070009		2000-05-30	Dean et al.	
	6	6381679	B1	2002-04-30	Matsubara et al.	
	7	6687794	B2	2004-02-03	Malik	
	8	6539458	B2	2003-03-25	Holmberg	

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↓	9	6973542	B1	2005-12-06	Schmuck et al.	
	10	6782454	B1	2004-08-24	Damron	
	11	5652858		1997-06-29	Okada et al.	
	12	6848029	B2	2005-01-25	Coldewey	
	13	5822790		1998-10-13	Mehrotra	

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TD	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
TD	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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TD	1	6105129		2000-08-15	Meier et al.	
	2	6928521	B1	2005-08-09	Burton et al.	
	3	6598153	B1	2003-07-22	Flachs et al.	
	4	5875294		1999-02-23	Roth et al.	
	5	6073109		2000-06-06	Flores et al.	
	6	6871298	B1	2005-03-22	Cavanaugh et al.	
	7	5555432		1996-09-10	Hinton et al.	
↓	8	6145077		2000-11-07	Sidwell et al.	

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↓	9	6865666	B2	2005-03-08	Yoshida et al.	
	10	5590352		1996-12-31	Zuraski et al.	
	11	6199204	B1	2001-03-06	Donohue	
	12	6202207	B1	2001-03-13	Donohue	

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↓	1	20050257092	A1	2005-11-17	Alexander et al.	
	2	20050177822		2005-08-22	Kuch et al.	
	3	20030041096	A1	2003-02-27	Johnson	

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